



PATENT
Atty Dkt. AMAT/6493/ETCH/SILICON/JB

2836

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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ARRIVED
Date Application of:
Padmapani C. Nallan, et al.
Serial No.: 09/993,240
Confirmation No.: Unknown
Filed: November 13, 2001
For: Apparatus For Controlling
A Thermal Conductivity
Profile For A Pedestal In A
Semiconductor Wafer
Processing Chamber

OFFICIAL DRAFTSPERSON
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Group Art Unit: Unknown
Examiner: Unknown
Batch No.: Unknown

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MAR 14 2004

CERTIFICATE OF MAILING 37 CFR 1.8	
<p>I hereby certify that this correspondence is being deposited on <u>2/18</u>, 2002 with the United States Postal Service as First Class Mail in an envelope addressed to: Official Draftsperson, Assistant Commissioner for Patents, Washington, D.C. 20231.</p> <p><u>2/18/02</u> <u>B. Todd Patterson</u> Date Signature</p>	

SUBMISSION OF FORMAL DRAWINGS

Attached please find two (2) sheets of formal drawings, with labels identifying the application for which they are submitted. The Examiner is requested to substitute these formal drawings for the informal drawings used during the prosecution of the subject patent application.

Any comparison fee should be charged to Deposit Account No. 20-0782/6493/BTP. Two copies of this document are enclosed for your convenience. If any additional informalities are identified by the Examiner, please contact the undersigned attorney at (713) 623-4844.

Respectfully submitted,

B. Todd Patterson

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